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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Confirmation No.: 9664

NAKAGAWA, et al.

Art Unit: 2818

Serial No.: 09/810,499

Examiner: C. Yoha

Filed: March 19, 2001

Docket No.: 100353-00049

For: SEMICONDUCTOR MEMORY DEVICE WITH SHORTER SIGNAL LINES

**AMENDMENT**

Commissioner for Patents  
Washington, D.C. 20231

April 21, 2003

Sir:

In response to the outstanding Office Action dated November 20, 2002, (hereinafter "outstanding Action"), the time for reply having been duly extended until April 21, 2003, (April 20 being a Sunday) by the attached petition, please amend the application as follows:

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**IN THE DRAWINGS**

Attached is a Request for Approval of Drawing Corrections and amended Fig. 1, which has a --Related Art-- label added thereto.

**IN THE CLAIMS:**

Please cancel claims 1-3, 5-6 and 8-9 without prejudice or disclaimer.

Please amend claims 4 and 7 as follows:

4. (Amended) A semiconductor memory device, comprising :
- a plurality of input/output terminals;

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